

	Type	L #	Hits	Search Text	DBs
1	IS&R	L1	876	(251/129.04).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
2	BRS	L2	465	1 and (valve with (position or positioning))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
3	BRS	L3	32	2 and (limit adj switch)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
4	IS&R	L4	1198	(251/284).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
5	BRS	L5	2	4 and 2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
6	BRS	L6	61898	(valve with (position or positioning or location or monitor or monitored or monitoring or control or controlled or controlling or controller)) and ((limit or threshold or set) with switch)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
7	BRS	L7	1810	6 and (((limit or threshold or set) with switch) same memory) same (microprocessor or computer or processor or processing or cpu or control or controller))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
8	BRS	L8	209	7 and ((valve with (position or positioning or location or monitor or monitored or monitoring)) with (sensor or sensing))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB

	Time Stamp	Comments	Error Definition	Errors
1	2005/06/13 10:09			
2	2005/06/13 10:12			
3	2005/06/13 09:20			
4	2005/06/13 10:09			
5	2005/06/13 10:10			
6	2005/06/13 10:26			
7	2005/06/13 10:39			
8	2005/06/13 10:29			

	Type	L #	Hits	Search Text	DBs
9	BRS	L9	40	6 and (((((limit or threshold or set) with switch) same memory) same (microprocessor or computer or processor or processing or cpu or control or controller)) same ((valve with (position or positioning or location or monitor or monitored or monitoring)) with (sensor or sensing)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB

	Time Stamp	Comments	Error Definition	Errors
9	2005/06/13 10:40			